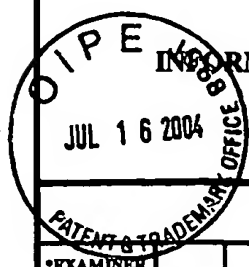


IDS - 02/13/2004 **ELECTRONIC INFORMATION DISCLOSURE STATEMENT**

Electronic Version v18

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Title of Invention	A Command Multiplier for Built-In-Self-Test																																																																																																																		
<p>Application Number :</p> <p>Confirmation Number:</p> <p>First Named Applicant: Jonathan Fales</p> <p>Attorney Docket Number: BUR920040037US1</p> <p>Art Unit:</p> <p>Examiner:</p> <p>Search string: ( 6647524 or 6583642 or 6574760 or 6611933 or 6360342 or 6055658 or 5614838 or 4903266 or 6550033 or 6001662 or 5954830 or 5388104 ).pn</p> <p><b>US Patent Documents</b></p> <p><b>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</b></p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td>CB</td><td>1</td><td>6647524</td><td>2003-11-11</td><td>Huang, et al</td><td></td><td>714</td><td>734</td></tr><tr><td>CB</td><td>2</td><td>6583642</td><td>2003-06-24</td><td>Huang, et al</td><td></td><td>324</td><td>765</td></tr><tr><td>CB</td><td>3</td><td>6574760</td><td>2003-06-03</td><td>Mydill</td><td></td><td>714</td><td>724</td></tr><tr><td>CB</td><td>4</td><td>6611933</td><td>2003-08-26</td><td>Koenemann, et al.</td><td></td><td>714</td><td>726</td></tr><tr><td>CB</td><td>5</td><td>6360342</td><td>2002-03-19</td><td>Lee, et al.</td><td></td><td>714</td><td>718</td></tr><tr><td>CB</td><td>6</td><td>6055658</td><td>2000-04-25</td><td>Jaber, et al.</td><td></td><td>714</td><td>726</td></tr><tr><td>CB</td><td>7</td><td>5614838</td><td>1997-03-25</td><td>Jaber, et al.</td><td></td><td>324</td><td>765</td></tr><tr><td>CB</td><td>8</td><td>4903266</td><td>1990-02-20</td><td>Hack</td><td></td><td>371</td><td>21.2</td></tr><tr><td>CB</td><td>9</td><td>6550033</td><td>2003-04-15</td><td>Dwork</td><td></td><td>714</td><td>733</td></tr><tr><td>CB</td><td>10</td><td>6001662</td><td>1999-12-14</td><td>Correale, Jr. et al.</td><td></td><td>438</td><td>11</td></tr><tr><td>CB</td><td>11</td><td>5954830</td><td>1999-09-21</td><td>Ternullo, Jr.</td><td></td><td>714</td><td>718</td></tr><tr><td>CB</td><td>12</td><td>5388104</td><td>1995-02-07</td><td>Shirotori, et al.</td><td></td><td>371</td><td>21.1</td></tr></tbody></table> <p><b>Signature</b></p> <table border="1"><thead><tr><th>Examiner Name</th><th>Date</th></tr></thead><tbody><tr><td>/Cynthia Britt/</td><td>06/22/2006</td></tr></tbody></table>								init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	CB	1	6647524	2003-11-11	Huang, et al		714	734	CB	2	6583642	2003-06-24	Huang, et al		324	765	CB	3	6574760	2003-06-03	Mydill		714	724	CB	4	6611933	2003-08-26	Koenemann, et al.		714	726	CB	5	6360342	2002-03-19	Lee, et al.		714	718	CB	6	6055658	2000-04-25	Jaber, et al.		714	726	CB	7	5614838	1997-03-25	Jaber, et al.		324	765	CB	8	4903266	1990-02-20	Hack		371	21.2	CB	9	6550033	2003-04-15	Dwork		714	733	CB	10	6001662	1999-12-14	Correale, Jr. et al.		438	11	CB	11	5954830	1999-09-21	Ternullo, Jr.		714	718	CB	12	5388104	1995-02-07	Shirotori, et al.		371	21.1	Examiner Name	Date	/Cynthia Britt/	06/22/2006
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**INFORMATION DISCLOSURE CITATION***(Use several sheets if necessary)*

Docket Number (Optional)

**BUR920040037US1**

Application Number

**10/708184**

Applicant(s)

**FALES ET AL.**

Filing Date

**02/13/04**

Group Art Unit

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

**U.S. PATENT APPLICATION PUBLICATIONS**

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

**FOREIGN PATENT DOCUMENTS**

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
CB		JP2003229000A	06/26/03	JAPAN				
CB		JP7078495A	03/20/95	JAPAN				

**OTHER DOCUMENTS** *(Including Author, Title, Date, Pertinent Pages, Etc.)*

CB		"A 800MHz Embedded DRAM with a Concurrent Refresh Mode", Toshiaki Kiriata et al., IBM Microelectronics, Hopewell Junction, NY, pp. 1-6.
CB		"Fault Simulation on the YSE/EVE", Barzilai et al., IBM Technical Disclosure Bulletin, Vol. 28, No. 2, July 1985, pp.773-776.

EXAMINER /Cynthia Britt/

DATE CONSIDERED 06/22/2006

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